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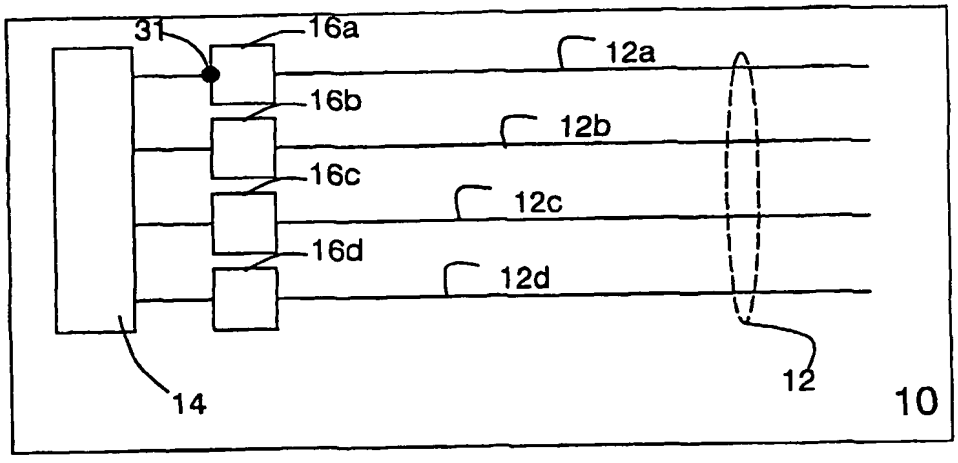
(54) Title: ELECTRIC DEVICE WITH DATA COMMUNICATION BUS



(57) Abstract: The electronic device (10) has a data communication bus (12) consisting of a plurality of substantially parallel conductors (12a, 12b, 12c, 12d). A control circuit (14) controls the values driven onto the conductors (12a, 12b, 12c, 12d). Transition dependent delay elements (16a, 16b, 16c, 16d) are coupled between the control circuit (14) and the respective conductors (12a, 12b, 12c, 12d) to delay certain transitions on the data communication bus 12. In particular, one of the opposite transitions on neighboring conductors e.g. a first conductor (12a) and a second conductor (12b) is delayed, thus reducing the power required to charge the mutual capacitance between the first conductor (12a) and the second conductor (12b). Consequently, a data communication bus (12) with reduced power consumption is obtained.

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